Minor Courses offered by the Department of Computer Science

Year / Sem	Type of Course	Course Code	Title of the Course	Credits	Teaching Hours
1 st Year (Semester- I & II)	MID-1	CS1MI01	Microprocessor and ALP	4	5
	MID-2	CS2MI02	Microcontrollers Programming	4	5
2 nd Year	MID-3	CS3MI03	System Software	4	5
(Semester- III & IV)	MID-4	CS4MI04	Embedded Application Development	4	5
3 rd Year (Semester- V & VI)	MID-5	CS5MI05	Theory of Computation	4	5
	MID-6	CS6MI06	UNIX System Programming / Network Programming	4	5
4 th Year (Semester- VII)	MID-7		AI / Compiler Design	4	5
	MID-8		Cyber Security / Internet of Things	4	5

Year	I	Course Code: CS1MI01		Credits	4
Sem.	1	1		Hours	75
		Course Title: Microprocessor & ALP			
Course	Numb	per Systems (binary, octal, hexade	ecimal) and their conversions.		
Prerequisites, if	Boolean Algebra, logic gates, flip-flops and registers.				
any	Concepts in Combinational and Sequential logic.				
Internal	End Semester Marks: 75 Duration of ESA (Theory) : 03 hrs			hrs.	
Assessment	Duration of ESA (Practical) : 03 hrs.				
Marks: 25					
Course	•	Learn the architecture & organ	nization of 8085 Microprocessor.		
Outcomes	Understand and classify the instruction set of the 8085 Microprocessor.				
	 Apply the memory & I/O Interfacing with 8085 Microprocessor. 				
	Analyse the architecture and operation of Programmable Interface.			0005	
	•	Create applications to interface various peripheral it is with intel 808 microprocessor			0005
Linit No		Course Content		Hours	
		Theory Compo	nent	Tiours	
	Intro	duction to Microprocessors & 80	85 Assembly Language	9	
	Progr	amming		5	
Linit I	Micro	pprocessors, Instruction set an	d computer languages, 8085		
Onici	progr	amming model. Instruction cl	assification. Instruction. Data		
	format and storage Execute a simple program 2025 instruction, of				
				0	
	Micro	processor Architecture and its or	perations Memory I/O Devices	9	
l Init II	8085 MPLL 8085 based microcomputer memory interfacing 8155				
Onten	memory segment Interfacing Interfacing I/O devices: Basics				
	Interfacing input and output devices, memory mapped I/O				
	Progr	amming 8085		9	
	Instruction Set of 8085, Data Transfer, arithmetic, Logic, Branch,				
Unit III	Writing ALP and Debugging programs, Looping, Counting and Indexing,				
	16 bit Arithmetic instructions, Logic operations, Counters and Time				
	Delay				
	Inter	facing I/O Devices		9	
	Stack and subroutines, Restart, Conditional call and Return instruction,				
Unit IV	Advanced subroutine concepts, Code conversion, BCD Arithmetic and				
	16 bit operations, BCD- Binary conversion, Binary to BCD conversion,				
	to bipary conversion, BCD addition and subtraction				
	Intor	facing Poriphoral (I/O) and Appli		0	
	Interr	runts: 8085 Interrunt RST instru	ctions Software and Hardware	5	
	interrupts: 5055 interrupt, KST instructions, Software and HardWare				
Unit V	Restart as Software Instructions 8155 – Multinurnose programmable				
	Device. 8279 – Programmable Keyboard/Display Interface. 8255 –				
	Progr	ammable peripheral Interface			
Practical Component					
	1	. Assembly Language Programm	ing for Arithmetic Operations	30	
	like Addition, Subtraction, Multiplication and Division on 8,				
	16-bit data.				
	2. Assembly Language Programming for different logical				
Exercises	operations.				
	3. Assembly Language Programming for code conversions.				
	4. Assembly Language Programming for Sorting				
	5 2	Assembly Language Programm	ing for memory block transfer		
	1 0	. ASSCHING LANGUAGE FIUSIAIIIII	ing for memory block transfer.	1	

	7. Assembly Language Programming using subroutines.				
	8 Assembly Language Programming using counters and time				
	dolov				
	uelay.				
	(Many more programs can be included related to the programming				
	techniques of Microprocessor 8085)				
Recommended Learning Resources					
	1. Ramesh S. Gaonkar, Microprocessor – Architecture, Programming and				
	Applications with the 8085, Penram International Publisher, 6th Edition 2013.				
	2. Douglas V. Hall, Microprocessors and Interfacing, Tata McGraw Hill				
	publications, 2nd Edition, 2012.				
	3. Intel Corp: The 8085 / 8085A. Microprocessor Book – Intel marketing				
	communications, Wiley Inter Science publications, 1980. (Digitized:17 Nov				
Print Resources	2007) ISBN:0471035688_9780471035688				
	4 Nilesh B Bahadure Microprocessors - The 8086/8088 80186/80286				
	80386/80/86 and the Pentium Family 2010 PHI Learning ISBN-078-81-203-				
	5945-2.				
	5. Barry B. Brey, The INTEL MICroprocessors – 8086 / 8088, 80186 / 80188, 80286,				
	80386, 80486 Pentium and Pentium pro processor, Pentium II, Pentium III and				
	Pentium IV - Architecture, Programming and interfacing, PHI, 8 th Edition, ISBN				
	0-13-502645-8.				

Year	I	Course Code: CS2MI02		Credits	4
Sem.	I	Course Title: Microcontroller Programming		Hours	75
Course Prerequisites, if	Digital Logic Fundamentals, Microprocessor and ALP				
any Internal Assessment Marks: 25	End Semester Marks: 75Duration of ESA (Theory) : 03 hrs. Duration of ESA (Practical) : 03 hrs.			hrs. 3 hrs.	
Course Outcomes	 Learn the fundamentals of Microcontrollers. Understand the internal design of 8051 microcontroller along with the features and their programming. Analyse the on-chip peripherals of microcontrollers. Design different interfacing applications using microcontrollers and peripherals. Build systems using microcontrollers for real time applications. 				
Unit No.	Course Content			Hours	
Unit I	Mic Mic Inpu Seri	roprocessors and Microcontrol roprocessors vs Microcontrol ut/Output Pins – Ports - External al Data Input/Output - Interrupts	ers ers lers - 8051 Architecture - Memory - Counter and Timers - s.	9	
Unit II	Programming 8051Addressing Modes, External Data Moves, Code Memory Read-OnlyData Moves, PUSH and POP Opcodes, Data Exchanges - LogicalOperations - Arithmetic Operations - Jump and Call Opcodes.			9	
Unit III	8051 Microcontroller Design Microcontroller Specification – Design - Testing - Timing Subroutines - Lookup Tables for 8051 - Serial Data Transmission.			9	
Unit IV	Applications Keyboards – Displays - Pulse Measurement - D/A and A/D Conversions - Multiple Interrupts.			9	
Unit V	Serial Data Communication Network Configurations - 8051 Data Communication Modes.			9	
Practical Component					
Exercises	 Blinking LED Reading Analog Input Digital Counter with Seven-Segment Display Analog-to-Digital Conversion (ADC) 			30	

	5. UART Communication			
	6. PWM (Pulse Width Modulation) Control			
	7. Timer Interrupt - Using a timer interrupt to perform a task			
	at regular intervals			
	8. I2C Communication			
	9. External Interrupt			
	10. Temperature Sensor (DS18B20) Interface			
	11. Matrix Keypad Interface			
	12. LCD Display Interface			
	13. Traffic Light Controller			
	(Many more programs can be included related to the programming			
	8051 microcontroller)			
Recommended Learning Resources				
	1. Kenneth J. Ayala, The 8051 Microcontroller Architecture, Programming, and			
	Applications, West Publishing Company, USA, 1991.			
	2. Martin Bates, PIC Microcontrollers - An Introduction to Microelectronics,			
	Third Edition, Elsevier, 2011, ISBN: 978-0-08-0969 1 1-4			
Print Resources	3. Hubert Henry Ward, C Programming for the PIC Microcontroller-Demystify			
	Coding with Embedded Programming, Apress, UK, 2020. ISBN-13 (pbk): 978-			
	1-4842-5524-7;			
	ISBN-13 (electronic): 978-1-4842-5525-4			
	https://doi.org/10.1007/978-1-4842-5525-4			